

**Fr. Conceicao Rodrigues College Of Engineering**  
**Department of Artificial Intelligence and Data Science Engineering**

**S.E. (AI& DS) (semester IV) (2022-2023)**  
**Course Outcomes & Assessment Plan**

**Subject: Operating System(OS-CSC405)**

**Credits-3**

**Course Objectives:**

1. To equip students with the fundamental knowledge and basic technical competence in the field of Computer Graphics..
2. To emphasize the implementation aspect of Computer Graphics Algorithms.
3. To prepare the student for advanced areas and professional avenues in the field of Computer Graphics.

**Teaching Scheme**

Course Code	Course Name	Teaching Scheme			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical/Ora l	Tut	Credits
CSC405	Operating System	03	--	--	03	--	---	03
CSC405	Operating System Lab	--	02	--	--	1	--	01

**Examination Scheme**

Course Code	Course Name	Theory Marks				Term Work	Practical & Oral	Total
		Internal Assessment			End Sem Exam			
		Test 1	Test2	Avg				
CSC305	Operating System	20	20	20	80 (3hr)	--	---	100
CSC305	Operating System Lab					25	25	50

## Syllabus:

Module	Detailed Content	Hours
<b>1</b>	<b>Operating system Overview</b>	<b>4</b>
	1.1 Introduction, Objectives, Functions and Evolution of Operating System	
	1.2 Operating system structures: Layered, Monolithic and Microkernel	
	1.3 Linux Kernel, Shell and System Calls	
<b>2</b>	<b>Process and Process Scheduling</b>	<b>9</b>
	2.1 Concept of a Process, Process States, Process Description, Process Control Block.	
	2.2 Uniprocessor Scheduling-Types: Preemptive and Non-preemptive scheduling algorithms (FCFS, SJF, SRTN, Priority, RR)	
	2.3 Threads: Definition and Types, Concept of Multithreading	
<b>3</b>	<b>Process Synchronization and Deadlocks</b>	<b>9</b>
	3.1 Concurrency: Principles of Concurrency, Inter-Process Communication, Process Synchronization.	
	3.2 Mutual Exclusion: Requirements, Hardware Support (TSL), Operating System Support (Semaphores), Producer and Consumer problem.	
	3.3 Principles of Deadlock: Conditions and Resource, Allocation Graphs, Deadlock Prevention, Deadlock Avoidance: Banker's Algorithm, Deadlock Detection and Recovery, Dining Philosophers Problem.	
<b>4</b>	<b>Memory Management</b>	<b>9</b>
	4.1 Memory Management Requirements, Memory Partitioning: Fixed, Partitioning, Dynamic Partitioning, Memory Allocation Strategies: Best-Fit, First Fit, Worst Fit, Paging and Segmentation, TLB	
	4.2 Virtual Memory: Demand Paging, Page Replacement Strategies: FIFO, Optimal, LRU, Thrashing	
<b>5</b>	<b>File Management</b>	<b>4</b>
	5.1 Overview, File Organization and Access, File Directories, File Sharing	
<b>6</b>	<b>I/O management</b>	<b>4</b>
	6.1 I/O devices, Organization of the I/O Function, Disk Organization, I/O Management and Disk Scheduling: FCFS, SSTF, SCAN, CSCAN, LOOK, C-LOOK.	

### Textbooks:

1	William Stallings, Operating System: Internals and Design Principles, Prentice Hall, 8 <sup>th</sup> Edition, 2014, ISBN-10: 0133805913 • ISBN-13: 9780133805918.
2	Abraham Silberschatz, Peter Baer Galvin and Greg Gagne, Operating System Concepts, John Wiley & Sons, Inc., 9 <sup>th</sup> Edition, 2016, ISBN 978-81-265-5427-0

### References:

1	Achyut Godbole and Atul Kahate, Operating Systems, McGraw Hill Education, 3 <sup>rd</sup> Edition
2	Andrew Tannenbaum, Operating System Design and Implementation, Pearson, 3 <sup>rd</sup> Edition.
3	Maurice J. Bach, "Design of UNIX Operating System", PHI
4	Sumitabha Das, "UNIX: Concepts and Applications", McGraw Hill, 4 <sup>th</sup> Edition

<b>Assessment:</b>	
<b>Internal Assessment:</b>	
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approx. 40% syllabus is completed and second class test when additional 40% syllabus is completed. Duration of each test shall be one hour.	
<b>End Semester Theory Examination:</b>	
1	Question paper will comprise of 6 questions, each carrying 20 marks.
2	The students need to solve total 4 questions.
3	Question No.1 will be compulsory and based on entire syllabus.
4	Remaining question (Q.2 to Q.6) will be selected from all the modules

<b>Useful Links</b>	
1	<a href="https://swayam.gov.in/nd1_noc19_cs50/preview">https://swayam.gov.in/nd1_noc19_cs50/preview</a>
2	<a href="https://nptel.ac.in/courses/117/106/117106113/">https://nptel.ac.in/courses/117/106/117106113/</a>
3	<a href="https://www.classcentral.com/course/swayam-introduction-to-operating-systems-6559">https://www.classcentral.com/course/swayam-introduction-to-operating-systems-6559</a>

## Lecture Plan : SEM III-CSC305

### Modes of Content Delivery:

i	Class Room Teaching	v	Self-Learning Online Resources	ix	Industry Visit
ii	Tutorial	vi	Slides	x	Group Discussion
ii i	Remedial Coaching	vii	Simulations/Demonstrations	xi	
i v	Lab Experiment	vii i	Expert Lecture	xi i	

Term : Jan 10- 22Apr 2023 (UT1 : 1 March-3 March) (UT2 : 17Apr-19 Apr)

### LECTURE PLAN:

Sr. No.	Topic Planned	Planned Date	Actual Date	Delivery Mechanisms
1.	Vision ,Mission of dept,Orientation towards NAAC	9/1/23	9/1/23	Board
2.	Course outcomes of operating system,exam scheme,introduction,functions,evolution of operating system	11/1/23	11/1/23	Board + PPT

3.	Kernel,types,shell,linux commands,system calls	12/1/23	12/1/23	Board + PPT
4.	Process and Program, Datastrcts related to Process in MM, Metadata of Process, PCB, Process Table, Process States	16/1/23	16/1/23	Board + PPT
5.	Lecture on : Process States	18/1/23	18/1/23	Board + PPT
6.	Lecture: Scheduler and Types, Threads and Process	19/1/23	19/1/23	Board + PPT
7.	Lecture on: Types of Thread and their comparison, Intro. to Process Scheduling, FCFS	23/1/23	23/1/23	Board + PPT
8.	Lecture on : SJF and SRTF	25/1/23	25/1/23	Board + PPT
9.	Lecture on: Round Robin and Priority Algo(Premptive and Non preemptive)	30/1/23	30/1/23	Board + PPT
10.	Lecture on : Multilevel Queue scheduling Algo and Multilevel Feedback Queue Scheduling Algo, Intro to Process Synchronization	1/2/23	1/2/23	Board + PPT
11.	Scheduling problem and Intro to process sync	2/2/23	2/2/23	Board + PPT
12.	Process Synchronization	6/2/23	6/2/23	Board + PPT
13.	Process Synchronization	8/2/23	8/2/23	Board + PPT
14.	Process Synchronization and Threads in OS	9/2/23	9/2/23	Board + PPT
15.	Mutual exclusion requirements,TSL	13/2/23	13/2/23	Board + PPT
16.	Semaphores,types	15/2/23	15/2/23	Board + PPT
17.	Producer consumer problem	16/2/23	16/2/23	Board + PPT
18.	DEadlock,resource allocation graphs,prevention	20/2/23	20/2/23	Board + PPT
19.	Deadlockavoidance,Banker's Algorithm	22/2/23	22/2/23	Board + PPT

20.	Deadlock detection and recovery,Dining Philosper problem	23/2/23	23/2/23	Board + PPT
21.	Memory management requirements,memory partitioning	28/2/23	28/2/23	Board + PPT
22.	Dynamic partitioning,strategies for memory allocation	6/3/23	6/3/23	Board + PPT
23.	Best fit,worst fit,first fit	9/3/23	9/3/23	Board + PPT
24.	Problems on best fit,first fit and worst fit	13/3/23	13/3/23	Board + PPT
25.	Virtual memoryPaging,address translation,TLB	15/3/23	15/3/23	Board + PPT
26.	Demand paging,thrashing	16/3/23	16/3/23	Board + PPT
27.	Segmentation,address translation	20/3/23	20/3/23	Board + PPT
28.	Page replacement policies FIFO,LRU,Optimal	23/3/23	23/3/23	Board + PPT
29.	Problems on FIFO,LRU,Optimal	27/3/23	3/4/23	Board + PPT
30.	File organization,access,file sharing	3/4/23	5/4/23	Board + PPT
31.	I/o functions,I/o organization	5/4/23	6/4/23	Board + PPT
32.	Disk organization,disk scheduling algorithms	6/4/23	10/4/23	Board + PPT
33.	FCFC,SSTF,SCAN,CSCAN,LOOK, CLOOK	10/4/23	12/4/23	Board + PPT
34.	Problems on disk scheduling	12/4/23	13/4/23	Board + PPT
35.		13/4/23	-	

### **Course Outcomes: [Target 2.5]**

*After successful completion of the course students will be able to:*

<b>CO1</b>	Understand the objectives, functions and structure of OS
<b>CO2</b>	Analyze the concept of process management and evaluate performance of process Scheduling algorithms.
<b>CO3</b>	Understand and apply the concepts of synchronization and deadlocks



## **CO ASSESSMENT TOOLS**

	<b>Direct Methods (80%)</b>					<b>Indirect Methods (20%)</b>
	Test 1	Test 2	Lab	Assignment	University Theory Result	(100%)
<b>CSC604.1</b>	10%	10%	15%	5%	60%	(100%)
<b>CSC604.2</b>	10%	10%	15%	5%	60%	(100%)
<b>CSC604.3</b>	10%	10%	15%	5%	60%	(100%)
<b>CSC604.4</b>	10%	10%	15%	5%	60%	(100%)
<b>CSC604.5</b>	10%	10%	15%	5%	60%	(100%)
<b>CSC604.6</b>	10%	10%	15%	5%	60%	(100%)

## **Rubrics for Assignments**

**Class : S.E. AI & DS**

**Semester : III**

<b>Assignment No:</b>	
<b>Title:</b>	
<b>Date of Performance:</b>	
<b>Roll No:</b>	
<b>Name of the Student:</b>	

**Evaluation:**

<b>Indicator</b>	<b>Very Poor</b>	<b>Poor</b>	<b>Average</b>	<b>Good</b>	<b>Excellent</b>
<b>Timeline (2)</b>	More than three sessions late (0)	More than two sessions late (0.5)	Two sessions late (1)	One session late (1.5)	Early or on time (2)
<b>Organization (3)</b>	N/A	Very poor readability and not structured (0.5)	Poor readability and somewhat structured (1)	Readable with one or two mistakes and structured (2)	Very well written and structured without any mistakes (3)
<b>Level of content (3)</b>	N/A	Major points are omitted or addressed minimally (0.5)	All major topics are covered, the information is accurate.(1)	Most major and some minor criteria are included. Information is Accurate (2)	All major and minor criteria are covered and are accurate. (3)
<b>Depth of Knowledge(2)</b>	N/A	One answer correct(0.5)	Two answers correct(1)	Three answers correct(1.5)	Four answers correct(2)



## Department of AI & DS Engineering

### Rubrics for assessment of Experiment:

<u>Sr. No.</u>		<u>Exceed Expectation (EE)</u>	<u>Meet Expectation (ME)</u>	<u>Below Expectation (BE)</u>
<u>1.</u>	<u>On time submission</u> <u>Or completion</u> <u>(2)</u>	<u>Early or on time</u> <u>(2)</u>	<u>One session late</u> <u>(1)</u>	<u>More than one session late</u> <u>(0)</u>
<u>2.</u>	<u>Preparedness(2)</u>	<u>Awareness about experiment to be performed,</u> <u>Knows the basic theory related to the experiment very well. (2)</u>	<u>Managed to explain the theory related to the experiment.</u> <u>(1)</u>	<u>Not aware of the theory to the point.</u> <u>(0)</u>
<u>3.</u>	<u>Skill (4)</u>	<u>Structured and optimum performance</u> <u>(4)</u>	<u>Few steps are not appropriate</u> <u>(2)</u>	<u>Just managed</u> <u>(1)</u>
<u>4.</u>	<u>Documentation</u> <u>(2)</u>	<u>Lab experiment is documented in proper format and maintained neatly.</u> <u>(2)</u>	<u>Most of the report is documented in proper format but some formatting guidelines are missed.</u> <u>(1)</u>	<u>Experiments not written in proper format (0.5)</u>

