FACULTY PROFILE

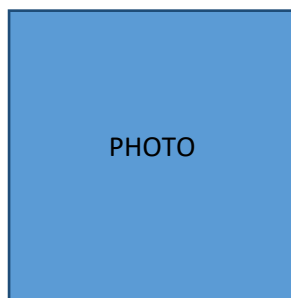|	
PHOTO	

NAME	: Heenakausar Yusuf Pendhari
DESIGNATION	: Assistant Professor
QUALIFICATION	: M.E. (Electronics)
EMAIL	: heenak.pendhari@frcrce.ac.in
AREA OF INTEREST	: Controls system, Digital circuits.

PROFESSIONAL BACKGROUND   :1) Assistant Professor, Fr.Conceicao Rodrigues college of   engg , Mumbai Since 22nd August 2013 .

2) Assistant Professor, Fr.Conceicao Rodrigues college of   engg. , Mumbai July 2010 to May 2013.

3) Technical cum Administrative Engineer, Thane, 1-12-2004 to 28-2-1006.

4) Visiting Lecture, Govt Polytechnic Ratnagiri 1997 to 2003.

EDUCATIONAL DETAILS   1) M.E. (Electronics), Fr. Conceicao Rodrigues college of Engg. Mumbai University, 2009

2) B.E. (Electronics), KIT college of Engg., Shivaji University, 1994.

3) DBM, Indian Merchant Chamber, Mumbai, 1997

ADMINISTRATIVE BACKGROUND   : Placement co-ordinator since 25th November, 2013

Ph.D. SUPERVISED   :   ---------

RESEARCH GRANT PROJECTS/PATENTS :   Smart Soil Analyzer (MRG) 2018-19

PUBLICATIONS   :

[1] Heenakausar Pendhari, Vaibhav Godbole" Interactive teaching- Learning Methodology using Moodle", National Symposium on Innovations in Teaching- learning Process-2018. D.Y.Patil College of Engg, Akurdi, Pune, December 08, 2018.

[2] Heenakausar Pendhari, Sangeeta Parshionikar, Archana Lopes,"Generating Hardware Fingerprint of FPGA using Physically Unclonable Function", International Conference on Recent Innovations in Electrical, Electronics & Communication Engineering CFP18P98-PRI:978-1-5386-5994-6, KIII, Bhubaneswar, Odisa, July' 27th -28th 2018.

[3] Heenakausar Pendhari, Sangeeta Parshionikar, Archana Lopes, "Early Detection of Heart Disease Using BRANN", International Journal of Creative Research Thoughts (IJCRT), volume 6 issue 2 April 2018 (ISSN:2320-2882).

[4] Heenakausar Pendhari, Sachin Shinde , "Ultrasonic Inspection TOFD Using ANN in in National conference held at Watumull Institute of Electronics and Computer technology, worli, Mumbai, 2012

[5] Heenakausar Pendhari, Sachin Shinde '' Intelligent Autonomous Adaptive Control Using Artificial Neural Network " National conference held at Watumull Institute of Electronics and Computer technology, worli, Mumbai, 2011.

[6]Heenakausar Pendhari , Sanjana Repal "Personal Authentication by Iris recognition " National conference held at K. J .Somaiya College of Engg, Vidyavihar, Mumbai ,20-21 March, 2009.

[7] Heenakausar Pendhari , Sanjana Repal "The 2D wavelet and 2D curvlet transform for image processing ", National conference held at Dayananda Sagar College of Engg, Bangalore, 19th-21st March, 2009.

BOOKS AUTHORED : **NIL**

TRAINING PROGRAMS CONDUCTED : 1) 2 weeks STTP on" Simulation: Concept to Practice", May 14th – 25th ,2012

2) Two day workshop on "Hands on Training on PYTHON coding and Remote Lab Learning", 10th and 11th February 2017.

PARTICIPATION IN TRAINING PROGRAM :

1) Two day training on "Python Programming: Basics to Advance" 30th and 31st August 2019.

2) 12 Weeks NPTEL Course "Digital Electronic Circuits " JAN-APR 2019

3) One day workshop on ""Virtual laboratories in Engg, "March 9th 2019.

4) Two day workshop on "AI & Deep learning workshop", at FRCRCE, Bandra, July 21st –July 23rd 2018.

5) Eight weeks NPTEL course on "Developing Soft skills and Personality", July-September 2017.

6)One week training on" Basic Digital system: Theory and Practice", May 7th – 11th 2018

7) "Laboratory experimentation with Cadence toolset for Analog VLSI design", 14th and 15th December 2016

8)"Hands-on Training on System Simulation Tools"(MULTISIM, LABVIEW& ULTIBOARD), August 5th – 6th, 2016

9) "Design Aspects in CMOS Analog Circuits, ASIC, and MEMS", June 27th – July 1 , 2016.

10) Two day workshop on "Teaching and Beyond: Widening Horizons", February 5th – 6th, 2016.

11) One day workshop on "NPTEL Awareness Workshop", January 8th 2016

12) One week STTP on" Analog CMOS VLSI Design", December 7th – 12th 2015

13) Two day workshop on "IC technology: Layout to Simulation", September 4th – 5th,2016.

14)Two day workshop on "Challenges in VLSI design", March 13th and 14th, 2015.

15) One day workshop organized by Accenture, January 27th 2015.

16) One Day Workshop on ARM Mbed Platform, March 8th, 2014.

17) Two day Workshop on Techniques for effective technical paper writing", August 30th-31st 2013

18) Two Week s SSTP on Basic Electronics conducted by IIT Bombay. 28th June – 8th July 2011

19) Eight weeks SSTP on Introduction to Information Technology and Computer Application conducted at Government Polytechnic Ratnagiri from 3rd July -25th July 2000.

14) National Conference on "Applications of Digital Signal Processing", January 19-20, 2007

INTERACTION WITH OUTSIDE WORLD   : NIL

PROFESSIONAL AFFILIATIONS   : NIL

OTHERS DETAILS   : NIL